

REMARKS

Claims 1-6, 9, 10, 12-14, 23, 24, 26, and 28 were rejected under 35 U.S.C. 102(b) as being anticipated by Varker or Baerg or Liu; and claims 7, 8, 11, 25, and 27 were rejected under 35 U.S.C. 103(a) as being unpatentable over Varker in view of Liu.

Claims 1-14, and 23-28 were canceled and claims 29-38 were added. The added claims all contain the limitation of implanting a species beneath the gate structure or beneath the channel region. This limitation is not found in either the Varker or Baerg or Liu patents where all the implanted species are confined to regions beneath the source and drain regions of the transistor. As such the added claims 29-38 are allowable over the cited art. Furthermore the Varker patent does not describe implanting species beneath the gate structure or in the channel region and so claims 29-38 are allowable over the Varker patent combined with the Liu patent.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.


While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicants petition for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including

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extension of time fees, to the deposit account of Texas Instruments Incorporated,
Account No. 20-0668.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Peter K. McLarty', with a long horizontal line extending to the right.

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